

Cisco demonstrated the benefits of its CPO solutions at OFC 2023, with a side-by-side comparison of the real power reduction between a conventional router with pluggable optical ...

Intel showed off a pretty cool piece of technology integrating an optical I/O chiplet with a CPU. The first iteration of the design is a fully integrated chiplet.

A failure in an optical engine might require replacing an entire CPO switch line card or server board rather than just swapping a pluggable module. Developing robust testing, diagnostics, ...

Intel paired one of its CPUs with an optical compute interconnect (OCI) chiplet.

We refer to this approach as Co-Packaged Optics (CPO) when applied to networking applications and Optical Compute Interconnect (OCI) when applied to compute fabrics

A co-packaged xPU (CPU, GPU, IPU) optical I/O solution can support higher bandwidths with high power efficiency, low latency, and longer reach, which is exactly what AI/ML infrastructure ...

NPO, or Near-Packaged Optics, is a highly integrated optical interconnect solution that falls between traditional pluggable optical modules and CPO.

Using advanced in-package optical I/O technology to interconnect xPUs, specifically CPUs, DPUs, GPUs, FPGAs, and ASICs, with memory and storage can help to achieve the necessary ...

At this year's Optical Fiber Communication Conference in San Francisco, the company demonstrated a system utilizing a new optical compute interconnect (OCI) chiplet featuring an ...

Intel has demonstrated an optical chiplet co-packaged with a CPU capable of supporting 4 Tbps data links to feed the increasing datacenter bandwidth requirements of AI and high ...



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